Northeastern University CS5600: Computer Systems: Fall 2021

Final Exam

1:40

• This exam is 120 minutes • Stop writing when "time" is called. You must turn in your exam; we will not collect it. Do not get up or pack up in the final ten minutes. The instructor will leave the room 123 minutes after the exam begins and will not accept exams outside the room. • There are h_{1} problems in this booklet. Many can be answered quickly. Some may be harder than others, and some earn more points than others. You may want to skim all questions before starting. • This exam is closed book and notes. You may not use electronics: phones, tablets, calculators, laptops, etc. You may refer to ONE two-sided 8.5x11" sheet. • If you find a question unclear or ambiguous be sure to write any assumptions you make • Follow the instructions: if they ask you to justify something, explain your reasoning and any important assumptions. Write brief, precise answers. Rambling brain dumps will not work and will waste time. Think before you start writing so that you can answer crisply. Be neat. If we can't understand your answer, we can't give you credit! • If the questions impose a sentence limit, we will not read past that limit. In addition, a response that includes the correct answer along with irrelevant or incorrect content, will lose points. • Don't linger. If you know the answer, give it, and move on.

• Write your name and NUID on the document in which you are working the exam.

I (xx/10)	II (xx/10)	III (xx/10)	IV (xx/10)	V (xx/20)	VI (xx/20)	VII (xx/20)	Total (xx/100)

Do not write in the boxes below.

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A4
Cheat sheet
** common terms
** common cmds: ls, touch, cat, echo, cd, mkdir
4. virtual memory
  -- what is VM?
    VA => PA
  -- benefits
    (a) programability ("transparency")
    (b) protection
    (c) effective use of resources
  -- translation, paging
    -- multiple ways, but "paging" dominates
    -- idea: divide memory into pages
    -- [VPN][offset] => [PPN][offset]
                                                             P: (110:(4
  -- VPN => PPN
    -- multiple ways, but "multilevel page table" dominates - [((( - ()))
    -- x86-64 page tables:
      -- VA: [9] 9 9 9 (VPN)] [ 12 (offset) ]
      -- root (cr3)
      -- each 9-bits serves as an index into L1/2/3/4 page tables
      -- page offset (12 bits) points within the final page
  -- page walk (IMPORTANT)
                                   O3 Tada
                                                           PA
  -- page table entry
                                                PIF
    -- many useful bits
    -- P, U/S, R/W, D, A, ...
  -- performance
    -- one page walk has to load multiple pages (why?)
                                                           D(D)
    -- accelerate by TLB (translation lookaside buffer)
    -- a cache, with mapping of VPN to PPN
                                                          A+206
  -- where does OS live?
-- in every process's address space
    -- but process's code cannot access (why?)
    -- not entirely true now (post-Meltdown)
  -- page fault
    -- translating VA to PA "fails"
    -- triggers:
       either because it's not mapped in the page tables or because
       there is a protection violation.
    -- how it works:
      -- CPU prepares "trap frame"
      -- CPU transfers control to kernel's...
      -- ...page fault handler
      -- register cr2 has the faulting VA
  -- page fault usage
    -- many, name a few:
    -- overcommitting memory (canonical usage)
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-- distributed shared memory
   -- copy-on-write
  -- page replacement policy
    -- "cache eviction problem"
   -- many lovely algorithms, but too expensive to use
   -- one that simulates LRU but easy to implement: CLOCK
     -- will use hardware-assistant bit in PTE (which one? Access bit)
   -- thrashing
  -- mmap
   -- a systemcall
   -- map a chunk of file directly into memory space
   -- fast and easy to use (memory's interface)
   -- need supports from VM system
5. I/O, Disk, SSD
  -- Port-mapped I/O (PMIO)
   -- x86 instructions: inb, inw, outb, outw, ...
   -- example: read keyboard inputs (handout)
   -- example: set blinking cursor (handout)
  -- memory-mapped I/O (MMIO)
   -- memory interface (read/write)
   -- connect to device registers under the hood
   -- exmaple: VGA, write to screen (handout)
  -- polling vs. interrupts
    -- polling: CPU busy queries status
   -- interrupts: hardware signals the CPU when status changes
    -- pros vs. cons
  -- DMA vs. Programed I/O
   -- Programmed I/O: PMIO and MMIO
   -- DMA: direct memory access
        -- better way for large and frequent transfers
        -- usually go with interrupts but do not have to
    -- {DMA, programmed I/O} x {polling, interrupts}
  -- Disk
   -- geometry of a disk (handout)
      -- platter, cylinder, track, sector
      -- how it works
    -- performance characteristics
  -- SSD
    -- how it works (handout)
     -- flash bank, block, page
     -- operations: read (a page), erase (a block), program (a page)
      -- bummer: wear-out
     -- FTL: flash translation layer
      -- a log-structured FTL
    -- performance characteristics
6. File systems
  -- fs goals:
    -- persistence
    -- name a set of bytes (file)
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-- easy to remember where are files (directories)
  -- files
    -- map name and offset to disk blocks:
         {file,offset} --> disk address
    -- analogies to virtual memory
    -- file mapping types
      -- contiguous allocation
      -- linked files
      -- indexed files (Unix) 75560
    --Unix inode:
        metadata (permisssions, times, link count)
        ptr 1 --> data block
         ptr 2 --> data block ĸ
         ptr 11 --> indirect block
                                                                    date
                       ptr -->
                       ptr -->
         ptr 12 --> indirect block
         ptr 13 --> double indirect block-
         ptr 14 --> triple indirect block
                                                    indirec
  -- Directories
    -- goal: find a file
    -- Hierarchical directories (Unix)
    -- directory stored on disk just like regular files
      -- difference? metadata (mode) and file contents_
                                                         (name=>inode)
                                     hard lind
    -- links
                                                        linude
                                                 nule
       -- hardlink
       -- softlink
  -- CS5600 FS
     [IMPORTANT; see Lab4; I will skip here]
  --crash recovery
    --we want fs (data structures) to be *consistent* even after crashes
(e.g., power failure)
     --ad-hoc: fsck
    --COW fs
    --journaling
       --redo logging
       --undo logging
                                  D
                              ourra
       --both
                                       Cou
7. Security
  -- many topics
  -- authentication
    -- three ways to do: based on what you know, what you have, and what you
are
    -- how we store passwords today
    -- fancy authentication
  -- access control
    -- subj -[access]-> obj: pass or reject
    -- Unix: UID/GID, process, file
       -- process's UID
       -- file's permission bits: rwxrwxrwx
       -- dir's permission bits: rwxrwxrwx
       /setuid)
       --motivation: need to raise privilege level sometimes (like updating
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password)
                                                                   return
      --setuid bit: run a program on behalf of its owner
      --dangerous, attacks 1, 2, 3, 4
                                                          %ripa
                                                           %r6)
  -- stack smashing: buffer overflow
   -- stack frame & calling convention
   -- attack (handout)
                                                           w
   -- defenses
     -- nonexecutable stack (NX) [attack: ROP, BROP]
      -- stack canaries
                                   [attack: stack reading]
     -- address randomization
                                   [attack: brute-force (32bit) or refork
server architecture]
___
1. processes
- an abstraction of a machine
- process manipulation
  -- create process: fork()
   -- why not createProcess()?
  -- fork/exec separation
  -- parent and child
    -- orphan process and zombie process
- process's view
  -- memory
   -- [draw the memory view of a process]
   -- code, data, stack, and heap
    -- in C, what variables are in which memory area?
  -- registers
   -- %rip
   -- %rbp, %rsp
    -- others
  -- file descriptors (defer to later)
- assembly code (x86)
 -- pushq %rax
  -- popq %rax
  -- call 0x12345
  -- ret
- calling convention
  -- where is the arguments and where is the return value
  -- call-preserved & call-clobbered
-- stack frame and how function call works
  -- [saved %rbp; local variables; call-preserved regs; %rip]
  -- how it works?
-- process-kernel interaction
  -- system calls
  -- exceptions
  -- interrupts
-- system calls
  -- an interface between processes and kernels
 -- how to know a systems call's function?
    -- man 2 <syscall>
  -- important system calls:
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-- fork, execve, wait, exit
    -- open, close, read, write
    -- pipe, dup2
  -- (briefly) how syscall is implemented
-- file descriptors
  -- an abstraction: a file, a device, or anything that follows open/read/
write/close
  -- 0/1/2: stdin/stdout/stderr
  -- redirection and pipe
-- shell
  -- an interface between human and computer
  -- how it works; Lab2
    -- parse commands
    -- run commands (fork/exec)
    -- handle shell operators
      -- redirections
      -- pipe
      -- connecting ops (;, &&, ||)
      -- subshell
-- threads
  -- two threads in the same process share memory
     -- meaning: they share code segment, data segment (i.e., global
        variables), and heap (i.e., memory from malloc)
  -- threads have separate stack and registers
2. Concurrency
-- thread APIs
  -- thread create(function pointer, args)
  -- thread_exit()
  -- thread join(thread id)
[draw the dangerous concurrency world with a safe path]
-- safe path: how to write concurrency code
  -- a systematic way to address concurrency problem
    -- this is one way, but is a good way, probably the best way that we're
aware of
  -- Monitor (mutex + condition variable)
  -- 6 rules from Mike Dahlin
    -- memorize them!
  -- four-step design approach
  -- Mutex: providing mutual exclusion
    -- APIs
      -- init(mutex)
      -- acquire(mutex)
      -- release(mutex)
    -- providing a critical section
      -- entering c.s.: "I can mess up with the invariant"
-- leaving c.s.: "I need to restore the invariant"
  -- Condition variable: providing synchronization scheduling
    -- APIS
```

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-- cond init(cond)
      -- cond wait(cond, mutex)
      -- cond signal(cond)
      -- cond broadcast(cond)
   -- an important interface, cond wait(...)
     -- meaning: 2 steps,
         (1) unlock mutex and sleep (until cond signaled)
         (2) acquire mutex and resume executing
  -- other synchronization primitives
   -- semaphore
   -- barrier
   -- spinlocks
   -- reader-writer locks
   -- read-copy-update (RCU)
   -- deterministic multithreading
-- dangerous concurrency world: concurrency problems
  -- if not sequential consistency
   -- hard for human beings to understand
    -- cases in handout week3.b panel 4
  -- concurrency problems under sequential consistency
   -- no synchronization:
      -- multiple threads working on some shared state
      -- atomicity-violation (race conditions)
         [examples: linked list and producer/consumer in handout week3.a]
      -- order-violation
   -- with incorrect synchronization
      -- deadlock
        -- well-studied problem (and intellectually challenging!)
        -- four conditions
        -- solutions
      -- livelock
      -- starvation
      -- priority inversion
3. Scheduling
-- OS scheduling problem
  -- kernel needs to decide which process to run
  -- when does kernel need to make this decision?
    -- process state transition graph
       (ready, running, waiting, terminated)
    -- non-preemptive scheduler: running->terminated or running->waiting
    -- preemptive scheduler: all four transitions
  -- scheduling another process
    -- context switch
    -- it has a cost
-- scheduling metrics
  -- turnaround time
  -- response time
  -- fairness
-- CS5600 scheduling game
```

-- multiple processes: P1, P2, P3, ... -- each with arrival time and running time -- 1 CPU -- ignore context switch costs -- assume no I/O -- note: processes arrive at the beginning of each time slot -- six classic scheduling algorithms -- FIFO/FCFS -- SJF/STCF -- RR -- Priority -- MLFQ -- Lottery -- beyond our scheduling game -- incorporating I/O into scheduling -- predicting future (simulating SJF/STCF)

-- complicated scheduling in practice (Reconfigurable Machine Scheduling Problem)

-- scheduling has a lot to do with others factors, for example, mutex



Lab4: 1706 t 5 day 12/11 23:59