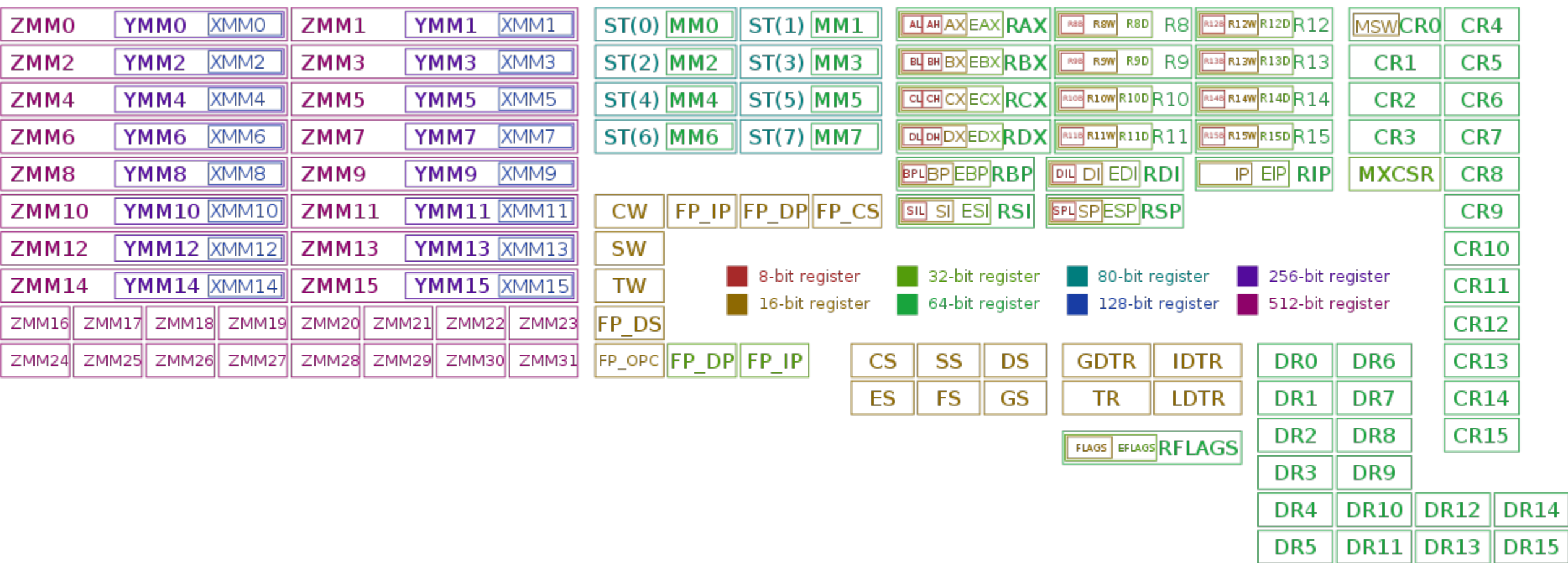
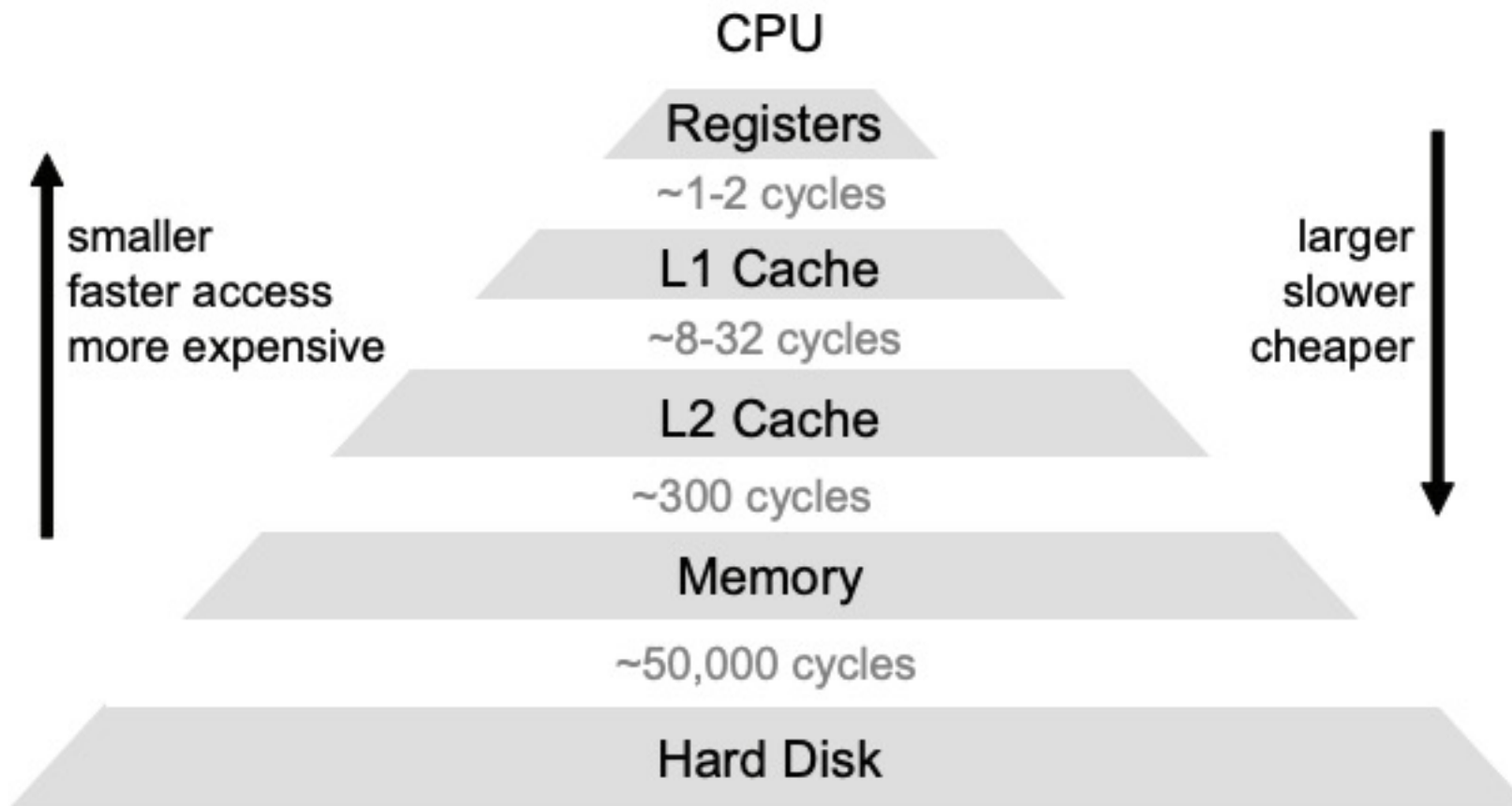


Borrowed from:

The Scalable Commutativity Rule: Designing Scalable Software for Multicore Processors

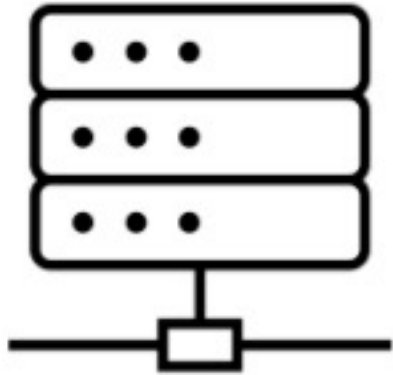
<https://pdos.csail.mit.edu/papers/commutativity:sosp13-slides.pdf>





Chellappa, S., Franchetti, F. and Püschel, M.
How To Write Fast Numerical Code: A Small Introduction

Figure 5, 2007



Within a Rack



< 10 μ s



Within a Datacenter



~ 1 ms



Over the World



> 100 ms

Borrowed from:

SymbioticLab. <https://symbioticlab.org/>

Register	Usage	Preserved across function calls
%rax	temporary register; with variable arguments passes information about the number of vector registers used; 1 st return register	No
%rbx	callee-saved register	Yes
%rcx	used to pass 4 th integer argument to functions	No
%rdx	used to pass 3 rd argument to functions; 2 nd return register	No
%rsp	stack pointer	Yes
%rbp	callee-saved register; optionally used as frame pointer	Yes
%rsi	used to pass 2 nd argument to functions	No
%rdi	used to pass 1 st argument to functions	No
%r8	used to pass 5 th argument to functions	No
%r9	used to pass 6 th argument to functions	No
%r10	temporary register, used for passing a function's static chain pointer	No
%r11	temporary register	No
%r12-r14	callee-saved registers	Yes
%r15	callee-saved register; optionally used as GOT base pointer	Yes

<https://github.com/hjl-tools/x86-psABI/wiki/x86-64-psABI-1.0.pdf>

Figure 3.4