Week 8.a CS3650 02/26 2024 https://naizhengtan.github.io/24spring/

- 1. Lab3
- 2. Survey
- 3. Midterm review
- 4. Q&A

Search

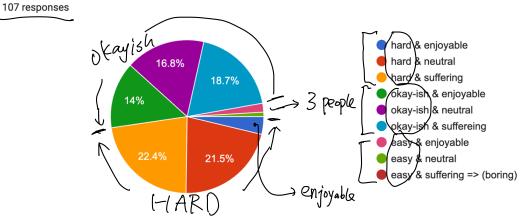
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Leaderboard

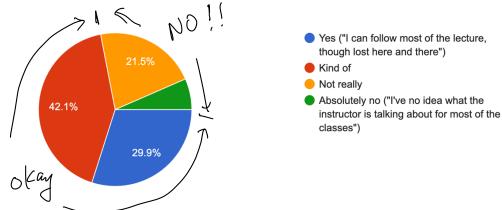
▲ Rank	¢ Submission Name	← Time taken (in seconds)		
1	Michelle	0.04 +20 points		
2	Nolan	0.1		
3	<u>TCA99</u>	0.27		
4	<u>Eric Sun</u>	0.39		
5	tom	11.64		
6	<u>Griffin Cooper</u>	^{12.06} / ≻(▷∂♂ _X		
7	sus	13.42		
8	Kenneth	31.06		
9	<u>sb</u>	V 31.89		
(10)	CS3650 Baseline	59.6 GOS		

Choose your current feeling about CS3650:

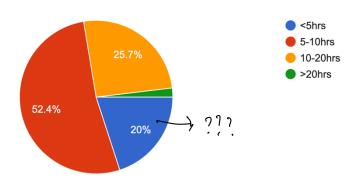




Do you agree, "I get the necessary background to follow lectures"? 107 responses



How many hours on average do you spend on CS3650 each week? 105 responses



```
CS3650 Midterm review
Overview
  1. computer organization & C
  2. processes
  3. concurrency & synchronization
  - Lab2: syscalls & shell
  - Lab3: concurrency programming
1. computer organization & C
- everything is 0s and 1s
  -- 1 byte == 8 bits
  -- integer: singed vs. unsigned; two's complement
  -- capacity: KB, MB, GB, TB, PB
  -- 1GB DRAM != 1GB disk
os 2^{10}B, 2^{20}, [2^{30}], 2^{40}, 2^{50}
                                                 (D<sup>6</sup>Bdisk
- OS
  -- providing services to user-level programs
  -- managing the resources
  -- abstracting the hardware

    hardware abstraction

 ∂- CPU
    -- ALUs and registers
 • memory: an array of bytes
-- address: index to the array
                                                                Stack
  - how a program (hellowor, ld) runs?
   * a file on disk
                        (trin/(S
   * load to memory; have code and data segmentes
   * the code segment is a sequence of instructions
                                                              Cod
                                                                  \cap
                                                                           ∦np
   * CPU needs to run the instructions
   * how does CPU know which instructions to run? CPU has a refister %rip
  (*) one way to see how a CPU works:
     while (true) {
       instruction <- fetch from memory pointed by %rip
       execute instruction
       %rip <- address of next instruction</pre>
     }

    a life cycle of a program

                 gcc
                                 as
                                              ld
          vi
    HUMAN ---> foo.c ---> foo.s ----> foo.o
                                                -> a.out
                                                                process
- C basics
  -- control flow
  -- functions and scope
  -- types and operators
     -- char, int, float, double
     -- precedence and associativity
  -- expectation: fluently read C code and write C code with minor syntax errors

    memory manipulation in C

  -- a pointer = a memory address
  -- C strings: a char array with terminating '\0'
  -- C arrays
                [1], +i
  -- struct and malloc/free
  -- printf
  -- main and arguments
2. processes
- an abstraction of a machine
- process manipulation
  -- create process: fork()
    -- why not createProcess()?
  -- fork/exec separation
  -- parent and child
    -- orphan process and zombie process
                                                  Stack (2 %rsp
🖉 )programer's view
```

⊖ memory data -- [draw the memory view of a process] codee -- code, data, stack, and heap '%riP -- in C, what variables are in which memory area? F) registers -- %rip -- %rsp Kernel PCB -- %rax : return value > (dev/fty -- others file descriptors (defer to later) - OS's view -- PCB -- file descriptor table context (registers and other metadata) -> context Switch > programs - system calls -- an interface between processes and kernels -- how to know a systems call? -- \$ man 2 <syscall> -- important system calls: -- open, close, read, write -> -- pipe, dup2 🗸 file descriptors -- an abstraction: a file, a device, or anything that follows open/read/write/close -- 0/1/2: stdin/stdout/stderr -- redirection and pipe - shell -- an interface between human and computer -- how it works; Lab2 -- parse commands > programs -- internal and external cmds -- how to tell? use "which -- run commands (fork/exec) -- handle shell operators -- redirections -- exit status (\$?) - assembly code (x86-64) -- movq A, B -- pushq %rax -- popq %rax -- call foo -- ret - calling convention -- where is the arguments and where is the return value -- call-preserved & call-clobbered stack frame and how function call works -- [saved %rbp; local variables; call-preserved regs; %rip] -- how it works? context switches -- switching between to processes -- trap to kernel; save context; schedule another process; restore context; resume -- processes do not realize being interrupted OS scheduling problem -- kernel needs to decide which process to run -- when does kernel need to make this decision? -- process state transition graph (ready, running, waiting, terminated) cx+ -- preemptive scheduler: all four transitions scheduling metrics stade1 -- turnaround time nen -- response time -- fairness Stackz Ł - RR: round-robin

neap

```
concurrency
                                                                           code
                                                                                      Τz
                                                                    T1 (9)34

    threads

                                                                                     %r`p
   -- inaccurate: "abstracting a CPU core"
                                                                    %rip
                                                                                     %r
                                                                     %r5P
  -- two threads in the same process share memory
      -- meaning: they share code segment, data segment (i.e.,
                                                                     global
         variables), and heap (i.e., memory from malloc)
   -- threads have separate stack and registers
  -- [will be crystal clear after studying virtual memory]
- thread APIs
  -- thread create(function pointer, args)
  -- thread exit()
  -- thread join(thread id)
- mental model: dangerous concurrency world
           [correct impl]
                         +--[safe path]
     Concurrency
                              N
     World 🥖 👩
     (dangerous)/
                  /all_sorts of
         0
                 / concurrency
                   bugs
             [you]<sup>-</sup>
  dangerous concurrency world: concurrency problems
   if not sequential consistency (hardware)
     -- hard for human beings to understand
     -- cases in handout week6.a panel 1
   • concurrency problems under sequential consistency
     -- no synchronization:
       -- multiple threads working on some shared state
       -- race condition
                                                proten
          [examples: broken linked list and producer/consumer in handouts]
      compiler optimization
       -- the example of printing 0s and 1s (scribble Week6.a)
     with incorrect synchronization
       -- potential concurrency bugs of violating rules
[e.g., using "if" instead of "while" to check waiting condition]
(-) safe path: how to write concurrency code
   -- safety first; this is one way, but is tested by time
  -- Monitor (mutex + condition variable)
   -- 6 rules from Mike Dahlin
     -- memorize them!
   -- four-step design approach

    synchronization primitives

   -- Mutex: providing mutual exclusion
     -- APIs
     \Im-- init(mutex)
       -- acquire(mutex)
       -- release(mutex)
     -- providing a critical section (mutual exclusion)
                           "I can mess up with the invariant"
       -- entering c.s.: "I can mess up with the invariant
-- leaving c.s.: "I need to restore the invariant"
   Condition variable:
     -- why? providing synchronization scheduling
        (motivating by the soda example)
     -- APIs
       -- cond_init(cond)
       -- cond_wait(cond, mutex) //
                                       ΊUGLY
       -- cond_signal(cond)
-- cond_broadcast(cond)
     -- an important interface, cond wait(...)
       -- meaning: 2 steps,
```

(1) unlock mutex and wait (until cond signaled)

```
(2) acquire mutex and resume executing
       -- why includes mutex (ugly interface)?
(-) four-step approach
4. we will cover lab2_and lab3

    do give a brief pass to your code

- expectation:
  -- if you've done your job, you can easily answer the questions
  -- if you're not, you still can but will spend a lot of time
notes about exam
  NOTE; bring your NUID
  a lot to read, not much to write
- a lot of code^{\ell}
  -- don't panic
  -- most lines are not important. They are there for completeness reasons.
  -- only several lines matter.
  (11) pages; A4; one-sided
   - stapled
  -- feel free to disassemble (easier to read code and answer questions)
  -- if you do so, remember to fill in your name and NUID 2 n the bottom of each page
- you will need to write code on paper
  -- we tolerate minor syntax errors
  -- you can always write down your assumptions
- you don't have to remember the "jargons"
   -- but you do need to remember "common terms"
  -- how to distinguish the two?
    Timagine you're a system programmer. When you talk to your peers, are you
     comfortable to say the words without explanation?
    -- "connecting input and output of two commands? Use pipe." {\cal L}
    (most people are okay with this.)
-- "try to predicting the future? Use EWMA." \nearrow
        (most people are not okay with this; EWMA means exponentially weighted
        moving average btw.)
    -- "what's the register to hold the 5th arguments of a function?" (
        (we will not ask such questions, as common programmers won't remember.
        The answer is $r8) btw.)
  we're reasonable people
  -- we're able to understand and reason things
  -- we're not idiots 🧲
  -- we will strictly follow the rules
      ("I don't know the rules [so they do not exist]" does not apply)
[broadcast the first page of the midterm]
 leak some information, but that's fine.
Q&A
Logistic questions
Content questions
  [shrug with no answer]
```

1

Northeastern University CS3650: Computer Systems: Spring 2024

Midterm Exam

• This exam is **90 minutes**.

- Stop writing when "time" is called. *You must turn in your exam; we will not collect it.* Do not get up or pack up in the final ten minutes. The instructor will leave the room 93 minutes after the exam begins and will not accept exams outside the room.
- There are 9 problems in this booklet. Many can be answered quickly. Some may be harder than others, and some earn more points than others. You may want to skim all questions before starting.
- This exam is closed book and notes. You may not use electronics: phones, tablets, calculators, laptops, etc.
- If you find a question unclear or ambiguous, be sure to write any assumptions you make.
- Follow the instructions: if they ask you to justify something, explain your reasoning and any important assumptions. Write brief, precise answers. Rambling brain dumps will not work and will waste time. Think before you start writing so that you can answer crisply. Be neat. If we can't understand your answer, we can't give you credit!
- If the questions impose a sentence limit, we will not read past that limit. In addition, *a response that includes the correct answer, along with irrelevant or incorrect content, will lose points.*
- Don't linger. If you know the answer, give it, and move on.
- Write your name and NUID on the document in which you are working the exam.

Do not write in the boxes below.

I (xx/24)	II (xx/24)	III (xx/28)	IV (xx/24)	Total (xx/100)

Name: