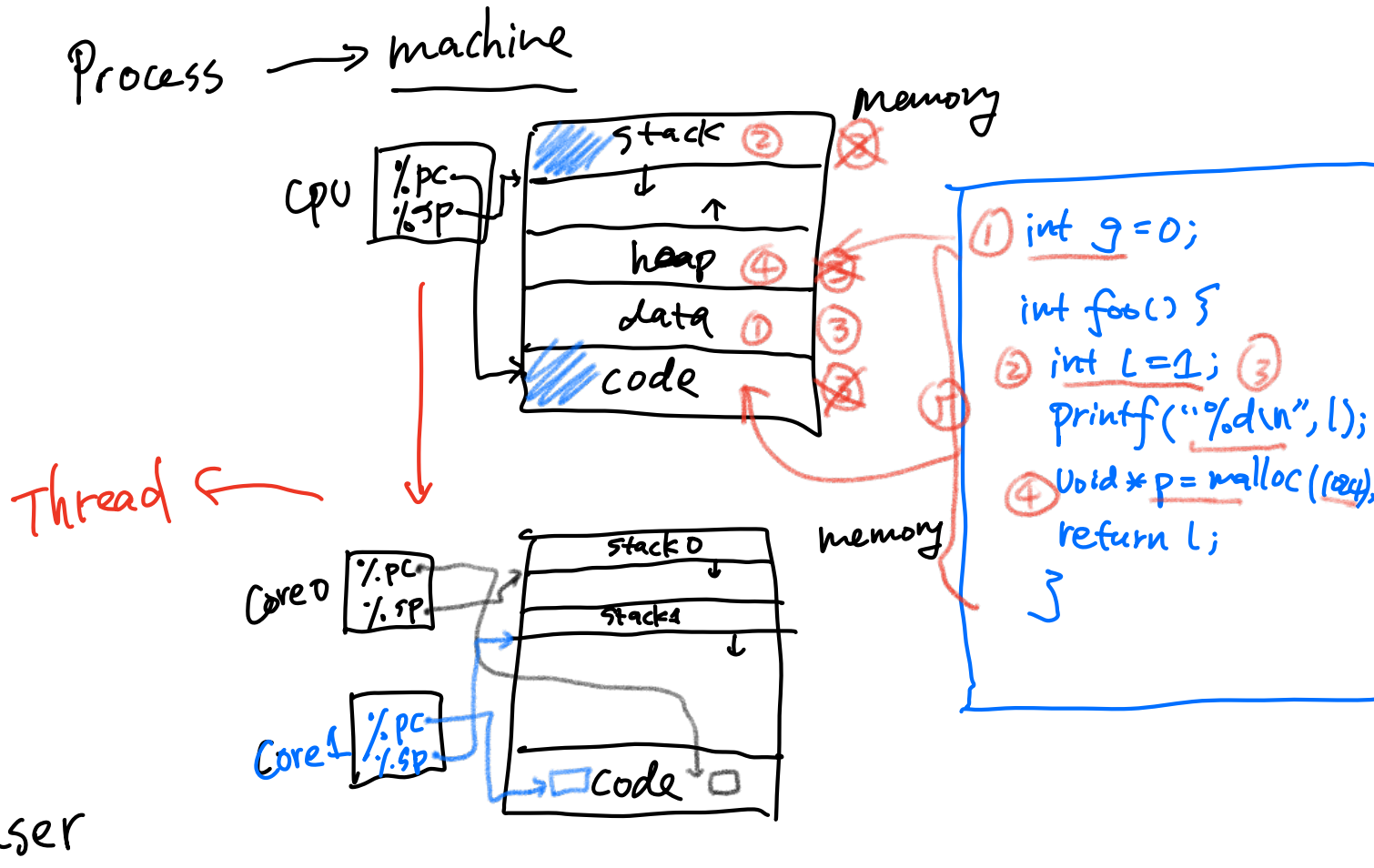


- ✓ 0. Admin, lab1, and lab2
- ✓ 1. Review: threading ←
- ✓ 2. Context switches in user-space
- ☐ 3. Cooperative vs. Preemptive multithreading in user-level } skip
- ☐ 4. Kernel debugging
- ✓ 5. Memory layout in egos ←
- ✓ 6. gdb ←

- office hour
- lab1
- PATH
- 0x2000 0000

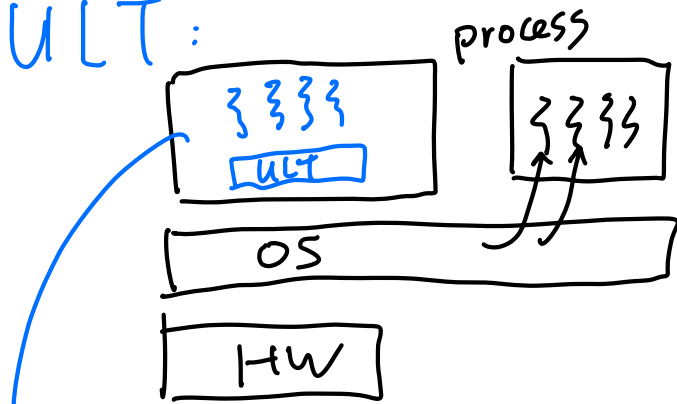
• Review



Kernel
 struct PCB {
 - registers
 - memory
 - fd array

struct TCB {
 sp
 }
 one thread

ULT:



1. manage threads (TCBs)
2. create threads (allocate stack)
3. scheduling
4. context switch (!)

• Context switch in user space

↳ states process context

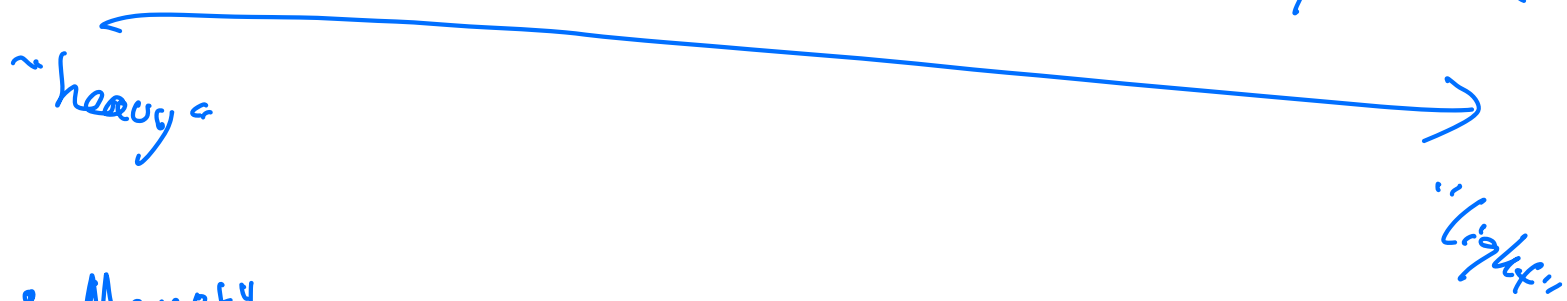
thread context ←

- A, B, C,
- A, B, C, D
- A, B, D
- ⇒ • A, B.
- A, B. (all other registers)

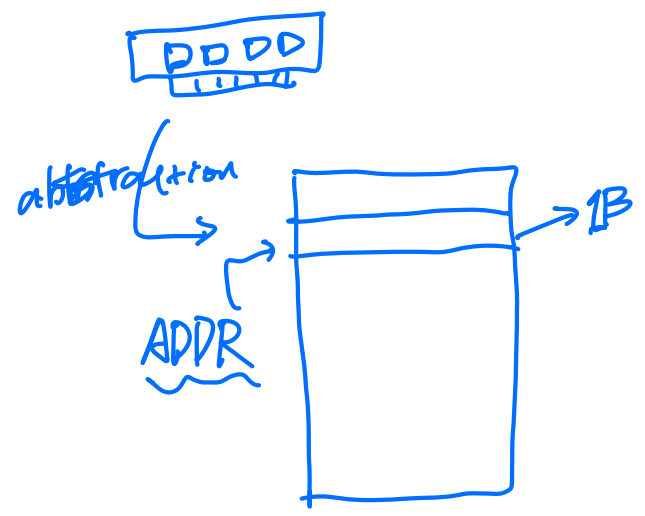
- A. %PC
- B. %SP
- C. stack
- D. heap
- E. other process memory

(ULT)

VM, Container, process, thread, fiber/coroutine (UL)



• Memory



Sifive_e

OSI Handout Week02.a

1. Background: RISC-V assembly I

a) registers [see "RISC-V registers" in reference page]

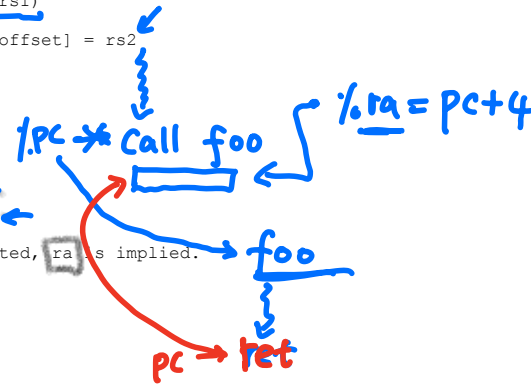
b) addi rd, rs1, immediate
rd = rs1 + immediate

opcode [operands]

c) sw rs2, offset(rs1)
Memory[rs1 + offset] = rs2

d) mv rd, rs1
rd = rs1

e) call rd, symbol
rd = pc+4
pc = &symbol



If rd is omitted, ra is implied.

f) ret
pc = ra

2. Context switch in user-space:

a) void ctx_start(void** old_sp, void* new_sp);

This will be used when starting a new thread. It will save registers on the old stack, store current stack pointer to "old_sp", switch stack to the "new_sp", and finally call ctx_entry().

b) void ctx_switch(void** old_sp, void* new_sp);

This will be used for context switch. It will save registers on the old stack, store current stack pointer to "old_sp", switch stack to the "new_sp", and restore registers from the new stack, finally return (to ra).

thread

*new-sp = SOMEWHERE
void * cur-sp;*

*ctx_switch(cur-sp, new-sp)
int a = 0;*

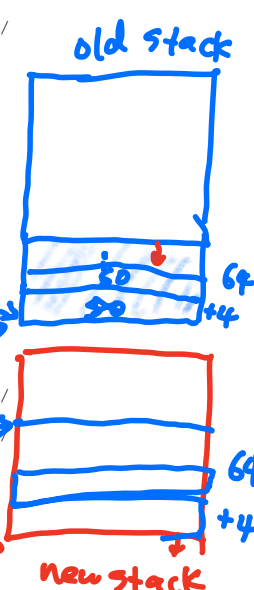
3. grass/context.S

```
1 ctx_start:
2   addi sp,sp,-64
3   sw s0,4(sp) /* Save callee-saved registers */
4   sw s1,8(sp)
5   sw s2,12(sp)
6   sw s3,16(sp)
7   sw s4,20(sp)
8   sw s5,24(sp)
9   sw s6,28(sp)
10  sw s7,32(sp)
11  sw s8,36(sp)
12  sw s9,40(sp)
13  sw s10,44(sp)
14  sw s11,48(sp)
15  sw ra,52(sp) /* Save return address */
16  sw sp,0(a0) /* Save the current stack pointer */
17  mv sp,a1 /* Switch the stack */
18  call ctx_entry /* Call ctx_entry() */
19
```

20 ctx_switch:

```
21   addi sp,sp,-64
22   sw s0,4(sp) /* Save callee-saved registers */
23   sw s1,8(sp)
24   sw s2,12(sp)
25   sw s3,16(sp)
26   sw s4,20(sp)
27   sw s5,24(sp)
28   sw s6,28(sp)
29   sw s7,32(sp)
30   sw s8,36(sp)
31   sw s9,40(sp)
32   sw s10,44(sp)
33   sw s11,48(sp)
34   sw ra,52(sp) /* Save return address */
35   sw sp,0(a0) /* Save the current stack pointer */
36   mv sp,a1 /* Switch the stack */
37   lw s0,4(sp) /* Restore callee-saved registers */
38   lw s1,8(sp)
39   lw s2,12(sp)
40   lw s3,16(sp)
41   lw s4,20(sp)
42   lw s5,24(sp)
43   lw s6,28(sp)
44   lw s7,32(sp)
45   lw s8,36(sp)
46   lw s9,40(sp)
47   lw s10,44(sp)
48   lw s11,48(sp)
49   lw ra,52(sp) /* Restore return address */
50   addi sp,sp,64
51   ret
```

sp = sp + (-64)



*old-sp -> %SP -> Void**

*'sp = a1
50 ←*

PC = ra

4. An example use of `ctx_start+ctx_entry`

```
void thread_create(void (*f)(void *), void *arg, unsigned int stack_size) {  
    tcb = create_thread_control_block();  
    old_tcb = current_running_thread_control_block();  
    ... // do something necessary  
  
    void **old_sp = ... // old stack pointer's address in old_tcb  
    void *new_sp = ... // new stack pointer in tcb  
  
    ctx_start(old_sp, new_sp);  
}  
  
void ctx_entry(void){  
    ... // do something useful  
    (*f)(arg); // run function "f" received by "thread_create"  
    ... // wrap up  
}
```

egos gdb:

- break at main of ult.c → check pid
- exception → something is wrong

* "normal" C program

- + you do not need to understand hardware details (like CPU)
- + you have clear error messages
- + you do not have to worry about touching important memory (the program will be killed)
- + you do not use addresses directly
- + you have a nice address space containing your program only
- + you have a lot of tools (like IDE)

