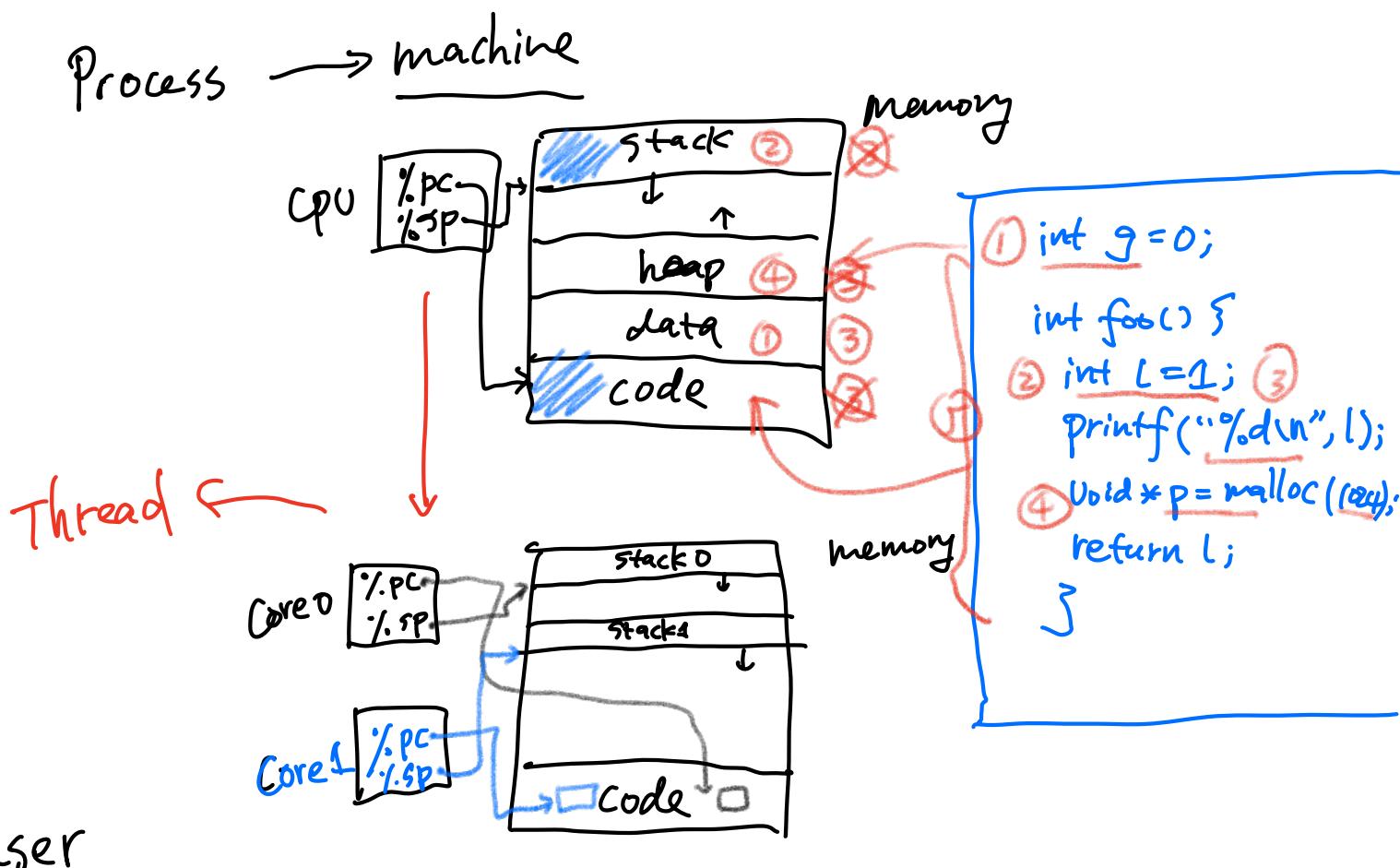


- 0. Admin, lab1, and lab2
  - 1. Review: threading ←
  - 2. Context switches in user-space
  - 3. Cooperative vs. Preemptive multithreading in user-level
  - 4. Kernel debugging
  - 5. Memory layout in egos ←
  - 6. gdb ←
- 

} skip

- office hour - PATH
- lab1 - 0x2000 0000

## • Review

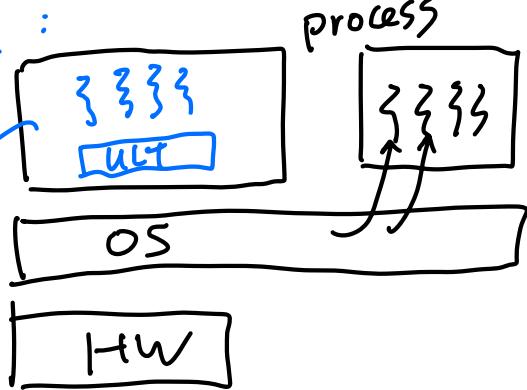


kernel  
struct PCB {  
- registers  
- memory  
- fd array  
}



Struct TCB {  
SP } → one thread

ULT:



1. manage threads (TCBs)
2. create threads (allocate **stack**)
3. scheduling
4. Context switch (!)

- Context switch in user space

↳ States      process context

thread context ←

- A, B, C,
- A, B, C, D
- A, B, D
- • A, B.
- A, B. (all other registers)

- A. %PC
- B. %SP
- C. stack
- D. heap
- E. other process memory

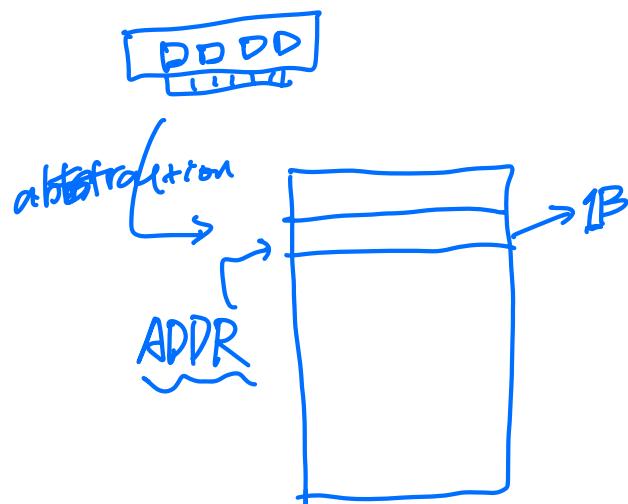
(ULI)  
VM, Container, process, thread, fiber/coroutine

~heavy~



~light~

- Memory



Sifive-e

## OSI Handout Week02.a

## 1. Background: RISC-V assembly I

a) registers  
[see "RISC-V registers" in reference page]

b) addi rd, rs1, immediate  
 $rd = rs1 + \text{immediate}$

c) sw rs2, offset(rs1)  
 $\text{Memory}[rs1 + \text{offset}] = rs2$

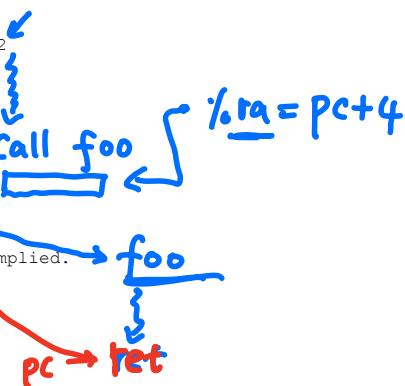
d) mv rd, rs1  
 $rd = rs1$

e) call rd, symbol  
 $rd = pc+4$   
 $pc = \&\text{symbol}$

If rd is omitted, ra is implied.

f) ret  
 $pc = ra$

opcode [oprands]



## 2. Context switch in user-space:

a) void ctx\_start(void\*\* old\_sp, void\* new\_sp);

This will be used when starting a new thread.  
It will save registers on the old stack,  
store current stack pointer to "old\_sp",  
switch stack to the "new\_sp",  
and  
finally call ctx\_entry().

b) void ctx\_switch(void\*\* old\_sp, void\* new\_sp);

This will be used for context switch.  
It will save registers on the old stack,  
store current stack pointer to "old\_sp",  
switch stack to the "new\_sp",  
and  
restore registers from the new stack,  
finally return (to ra).

thread  
.new\_sp = SOMEWHERE  
void \* cur\_sp;

Ctx\_Switch(&cur\_sp, new\_sp)  
int a = 0;

3. grass/context.S

```

1 ctx_start:
2     addi sp,sp,-64
3     sw s0,4(sp)
4     sw s1,8(sp)
5     sw s2,12(sp)
6     sw s3,16(sp)
7     sw s4,20(sp)
8     sw s5,24(sp)
9     sw s6,28(sp)
10    sw s7,32(sp)
11    sw s8,36(sp)
12    sw s9,40(sp)
13    sw s10,44(sp)
14    sw s11,48(sp)
15    sw ra,52(sp)
16    sw sp,0(a0)
17    mv sp,a1
18    call ctx_entry
19
20 ctx_switch:
21    addi sp,sp,-64
22    sw s0,4(sp)
23    sw s1,8(sp)
24    sw s2,12(sp)
25    sw s3,16(sp)
26    sw s4,20(sp)
27    sw s5,24(sp)
28    sw s6,28(sp)
29    sw s7,32(sp)
30    sw s8,36(sp)
31    sw s9,40(sp)
32    sw s10,44(sp)
33    sw s11,48(sp)
34    sw ra,52(sp)
35    sw sp,0(a0)
36    mv sp,a1
37    lw s0,4(sp)
38    lw s1,8(sp)
39    lw s2,12(sp)
40    lw s3,16(sp)
41    lw s4,20(sp)
42    lw s5,24(sp)
43    lw s6,28(sp)
44    lw s7,32(sp)
45    lw s8,36(sp)
46    lw s9,40(sp)
47    lw s10,44(sp)
48    lw s11,48(sp)
49    lw ra,52(sp)
50    addi sp,sp,64
51    ret

```

/\* Save callee-saved registers \*/

/\* Save return address \*/

/\* Save the current stack pointer \*/

/\* Switch the stack \*/

/\* Call ctx\_entry() \*/

/\* Save callee-saved registers \*/

/\* Save return address \*/

/\* Save the current stack pointer \*/

/\* Switch the stack \*/

/\* Restore callee-saved registers \*/

/\* Restore return address \*/

/\* Restore the current stack pointer \*/

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/\* Restore callee-saved registers \*/

/\* Restore return address \*/

/\* Restore the current stack pointer \*/

old stack  
old-sp → 1.sp → void\*  
a1 New-sp → 1.sp → 50 → 64 → +4  
new stack

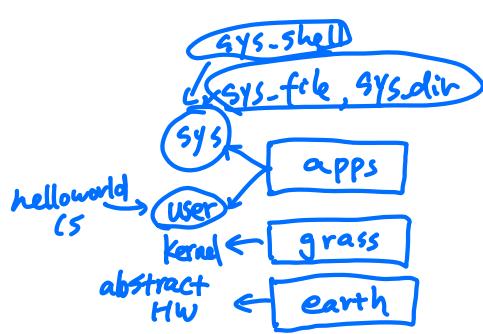
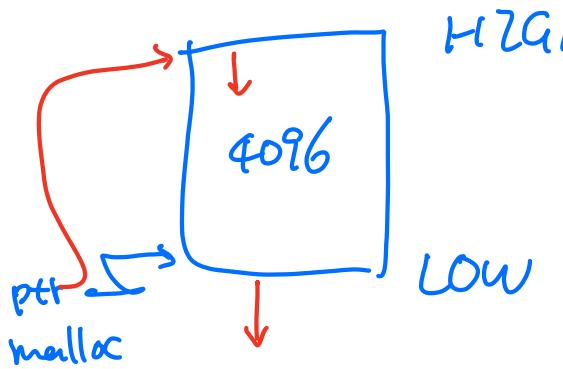
## 4. An example use of ctx\_start+ctx\_entry

```
void thread_create(void (*f)(void *), void *arg, unsigned int stack_size) {  
    tcb = create_thread_control_block();  
    old_tcb = current_running_thread_control_block();  
    ... // do something necessary  
    void **old_sp = ... // old stack pointer's address in old_tcb  
    void *new_sp = ... // new stack pointer in tcb  
    ctx_start(old_sp, new_sp);  
}  
  
void ctx_entry(void){  
    ... // do something useful  
    (*f)(arg); // run function "f" received by "thread_create"  
    ... // wrap up  
}
```

egos gdb:

- break at main of ult.c → check pid
- exception → something is wrong

- \* "normal" C program
  - + you do not need to understand hardware details (like CPU)
  - + you have clear error messages
  - + you do not have to worry about touching important memory (the program will be killed)
  - + you do not use addresses directly
  - + you have a nice address space containing your program only
  - + you have a lot of tools (like IDE)



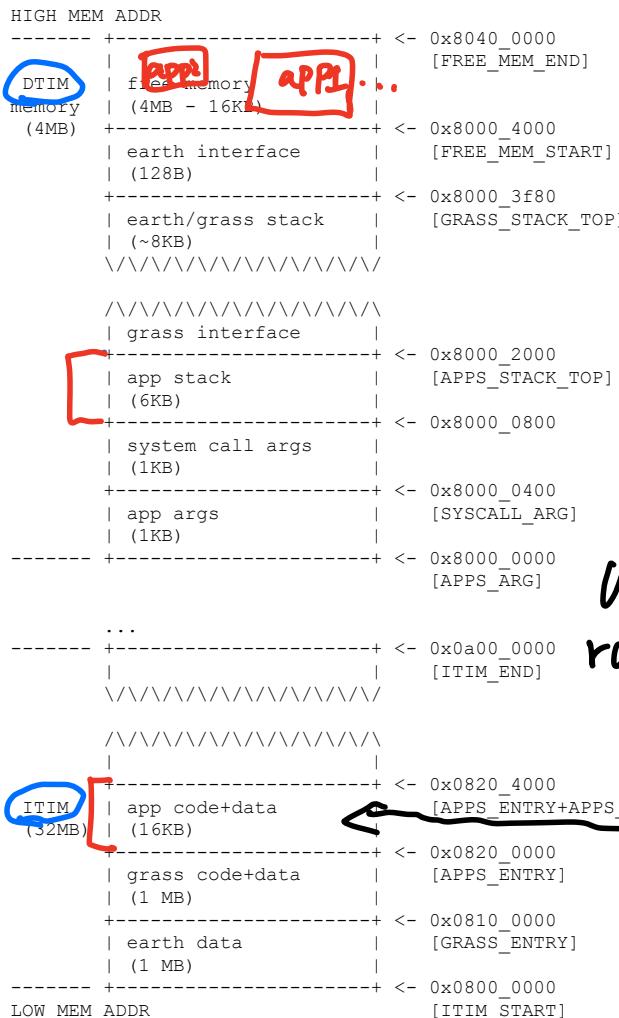
handout\_w02b

OSI, Cheng Tan

1/13/25, 8:53 AM

OSI Handout Week02.b

## 1. egos-2k+ memory layout (for the sifive\_e CPU)



Where is  
running app's  
heap?

-0x8201758

handout\_w02b

## 2. qdb cheat sheet

## Breakpoints & watchpoints

(gdb) break main	set a breakpoint on a function
(gdb) break ult.c:10	set breakpoint at file and line (or function)
(gdb) info breakpoints	show breakpoints
(gdb) delete 1	delete a breakpoint by number
(gdb) watch expression	set software watchpoint on variable
(gdb) info watchpoints	show current watchpoints

## Running the program

```
(gdb) c          continue the program
(gdb) s          a step in C; step into functions
(gdb) si         a step in asm; step into functions
(gdb) n          a step in C; step over functions
(gdb) ni         a step in asm; but step over functions
(gdb) CTRL-C     actually SIGINT, stop execution of current program
(gdb) finish      finish current function's execution
```

## Stack backtrace

```
(gdb) bt          print stack backtrace
(gdb) info locals   print automatic variables in frame
(gdb) info registers print registers sans floats
```

## Browsing Data

(gdb) p expr	print expression
(gdb) p/x expr	print in hex
(gdb) p/t expr	print in binary
(gdb) p/j expr	print as instructions

```
(gdb) x/FMT address      low-level examine command  
(gdb) x/x 0x80001000    print memory in hex  
(gdb) set var = expr     assign value
```

```
(gdb) display/FMT expr      display expression result at stop  
(gdb) display/i $pc        print next instruction  
(gdb) undisplay           delete displays
```

FMT (Format letters) are:  
o(octal), x(hex), d(decimal), u(unsigned decimal),  
t(binary), f(float), a(address), i(instruction), c(char), s(string)  
and z(hex, zero padded on the left).

Load a program's symbols

(gdb) add-symbol-file <elf> load symbol table from <elf>

## History Display

(gdb) show commands print command history

[borrowed and customized from

<https://qist.github.com/rkubik/b96c23bd8ed58333de37f2b8cd052c30>