

OSI Week7a

1. CSR satp (page table root)

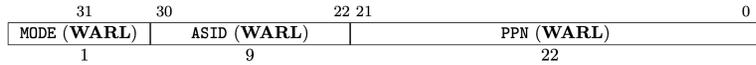


Figure 4.14: Supervisor address translation and protection register **satp** when SXLEN=32.

(1-bit MODE)

SXLEN=32		
Value	Name	Description
0	Bare	No translation or protection.
1	Sv32	Page-based 32-bit virtual addressing (see Section 4.3).

2. Page table entry (PTE)

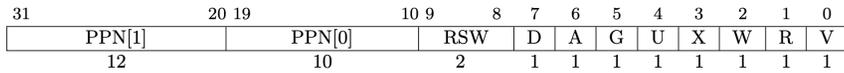


Figure 4.18: Sv32 page table entry.

3. Virtual address (VA)

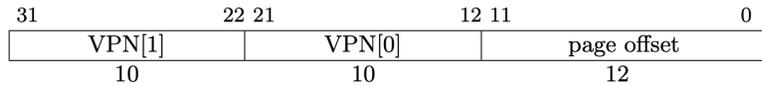


Figure 4.16: Sv32 virtual address.